WHAT IS CLAIMED IS:

1. A method for a physical page allocation of multiple memory chips comprises steps of:

defining N sequential pages as a page set, wherein N is a positive integer;

allocating a first page set into a first memory chip;

allocating a second page set into a second memory chip, wherein the second page set is sequentially next to the first page set;

allocating a third page set into the first memory chip; and

allocating a fourth page set into the second memory chip, wherein the fourth page set is sequentially next to the third page set.

2. The method according to Claim 1 wherein said memory is a flash memory.

3. The method according to Claim 1 wherein said memory size of each page is 512 Bytes.

4 The method according to Claim 1 wherein said memory size of each page set is 512*N Bytes.

5. The method according to Claim 1 wherein said allocation method is a 2-way interleave mode according to said third page next to said second page set.

6. A method for physical allocation for multiple memory chips comprises steps of:

Defining N sequential pages as a page set, wherein N is a positive integer;

Allocating a first page set into a first memory chip;

Allocating a second page set into a second memory chip, wherein said second page set is sequentially next to said first page set;

Allocating a third page set into a third memory chip;

Allocating a fourth page set into a fourth memory chip, wherein the fourth page set is sequentially next to the third page set.

Allocating a fifth page set into the first memory chip;

Allocating a sixth page set into the second memory chip, wherein the sixth page set is sequentially next to the fifth page set.

Allocating a seventh page set into the third memory chip; and

Allocating a eighth page set into the fourth memory chip, wherein the eighth page set is sequentially next to the seventh page set.

7. The method according to Claim 6 wherein said memory is a flash memory.

8. The method according to Claim 6 wherein said memory size of each page is 512 Bytes.

9. The method according to Claim 6 wherein said memory size of each page set is 512*N Bytes.

10. The method according to Claim 6 wherein said allocation method is a 2-way interleave mode according to the fifth page next to the second page set and the seventh page set next to the fourth page sets.

11. The method according to Claim 6 wherein said allocation method is a 4-way interleave mode according to the third page set next to the second page set, the seventh page set next to the sixth page set and the fifth page set next to the fourth page set.